

**INFORMATION DISCLOSURE
STATEMENT**

Atty Docket:
Serial No.:
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COFLER
February 26, 2002

**U.S. PATENT DOCUMENTS**

Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Dat
EC	AA	5,664,137	9/2/97	Abramson et al.	395	392	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub Class	Translation
EC	AL	0133477	2/27/85	EP	G06F9	38	
EC	AM	0840209	5/6/98	EP	G06F9	38	
EC	AN	1050805	11/8/00	EP	G06F9	38	
	AO						

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

EC	AP	Fossum et al., "Designing a Vax for High Performance" Computer Society International Conference, Spring Meeting, Los Alamitos, February 26 - March 2, 1990, Los Alamitos, IEEE Comp. Soc. Press, US, vol. CONF. 35, February 26, 1990, pages 36-43, XP000146163					
EC	AQ	Yoshida et al., "A Strategy for Avoiding Pipeline Interlock Delays in a Microprocessor" Proceedings of the International Conference on Computer Design: VLSI in Computers and Processors, Cambridge, MA., September 17-19, 1990, pages 14-19, XP000201400					
EC	AR	Zhang et al., "Performance Modeling and Code Partitioning for the DS Architecture" Computer Architecture News, Association for Computing Machinery, New York, US, vol. 26, no. 3, Specissue, June 1, 1998, pages 293-304, XP000784225					

EXAMINER:

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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